

Fig. 1

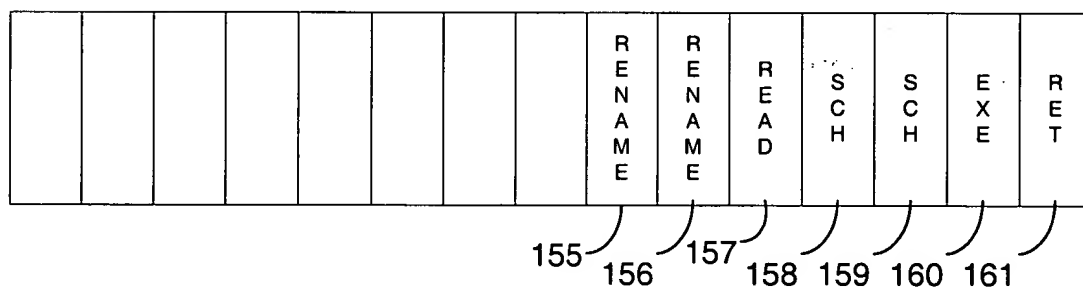


Fig. 1A

FIG. 1B

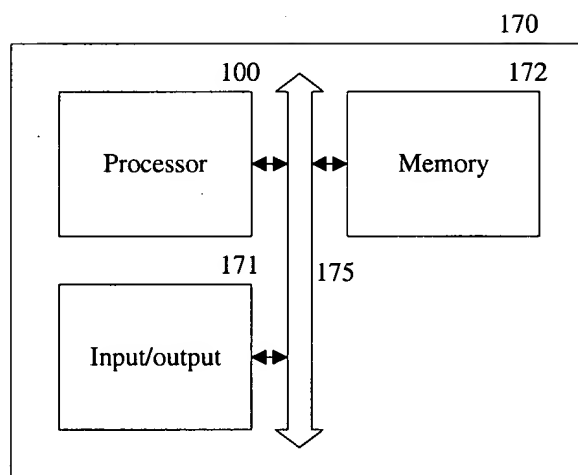


FIG. 1B

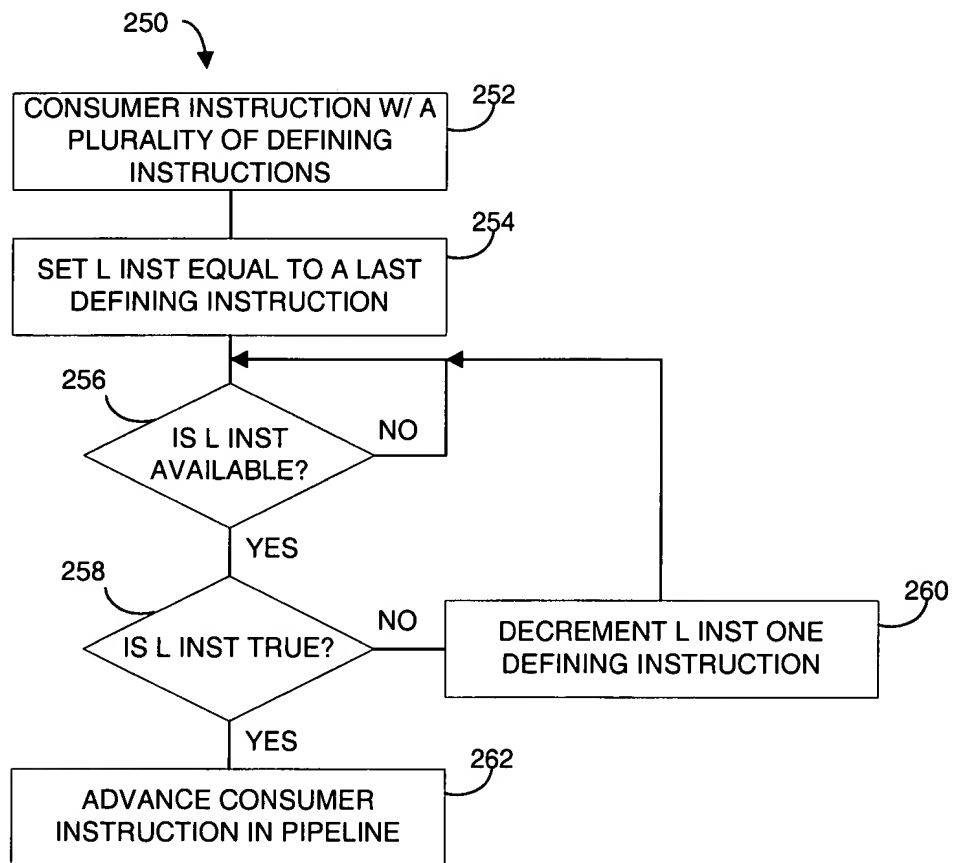


Fig. 2A

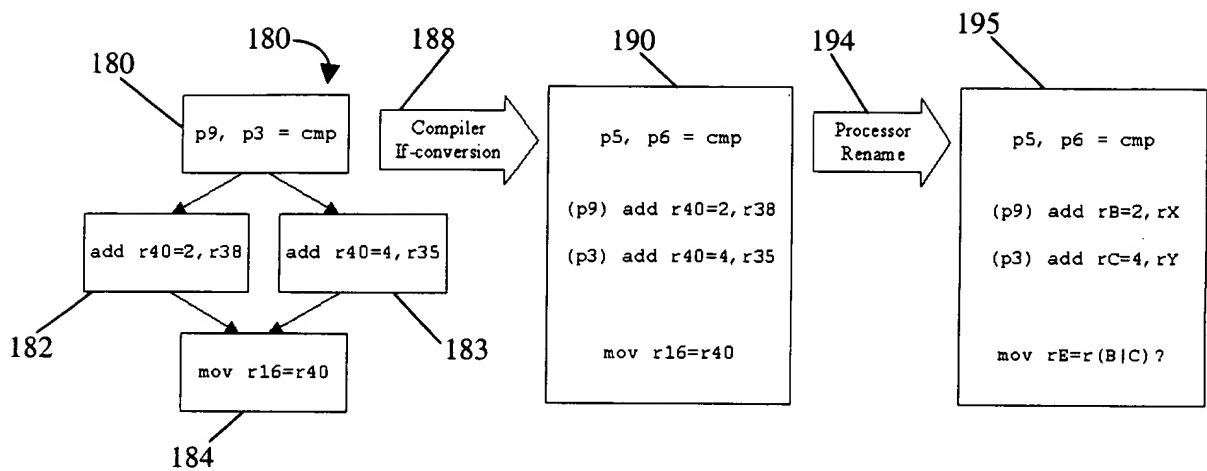


Fig. 1C

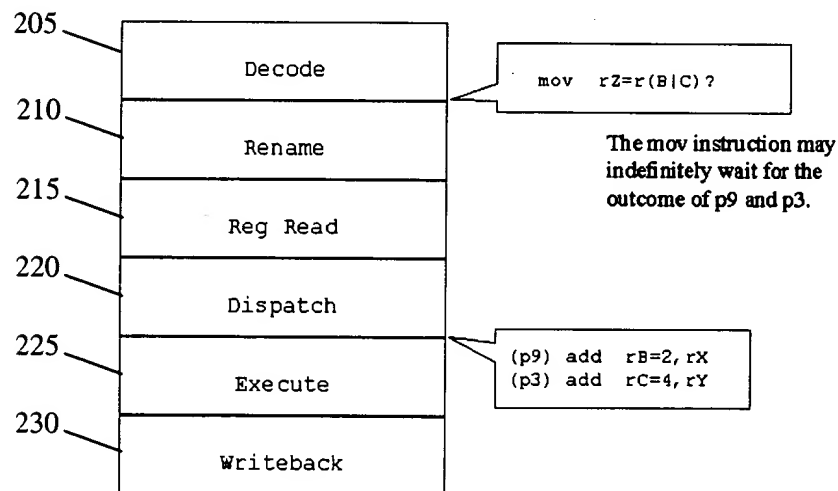


Fig. 2

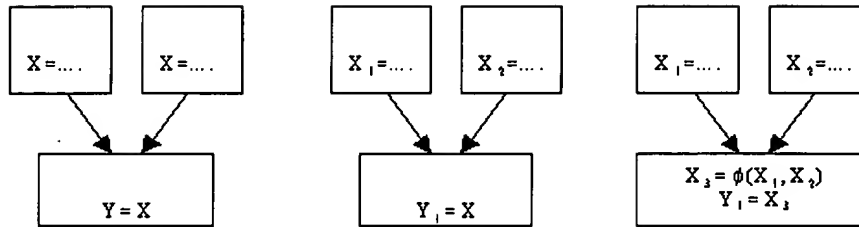


Fig. 3

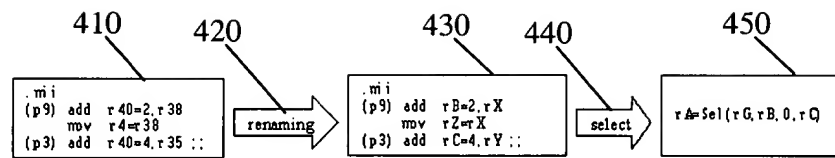


Fig. 4

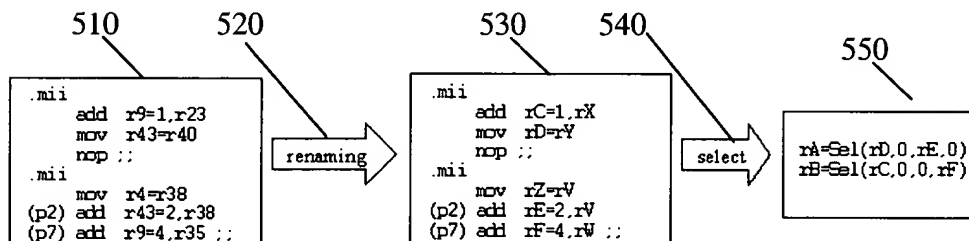


Fig. 5

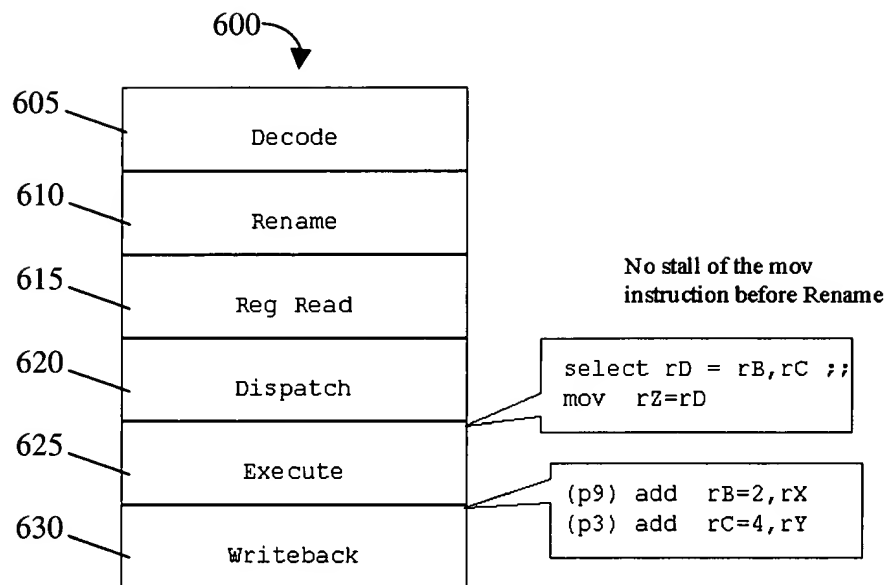


Fig. 6

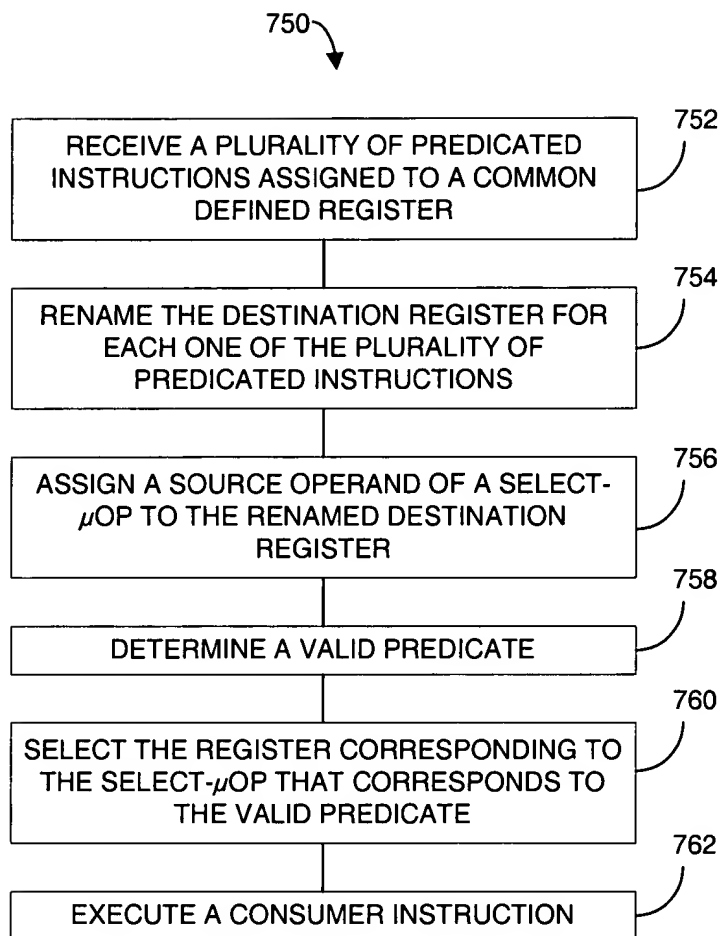


FIG. 7A

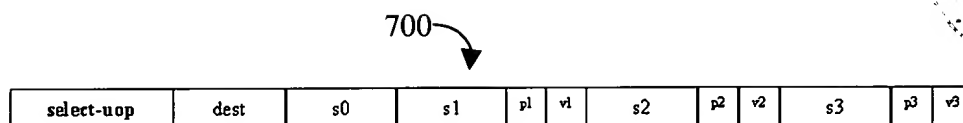


Fig. 7

	Slot 0			Slot 1		Slot 2		Slot 3	
	valid	phys reg	predicate	phys reg	predicate	phys reg	predicate	phys reg	predicate
0	1	r4	p1	r8	p9				
1	1	r11	p0						
2	0								
3	1	r6	p9	r14	p4	r21	p0	r17	p2
4	1	r24	p2						
..									
..									
..									
..									
..									
n	1	r7	p12	r15	p3				

Fig. 8

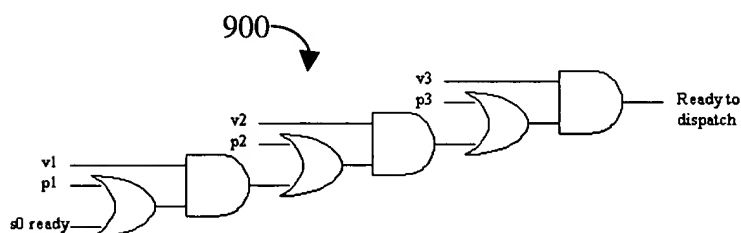


Fig. 9

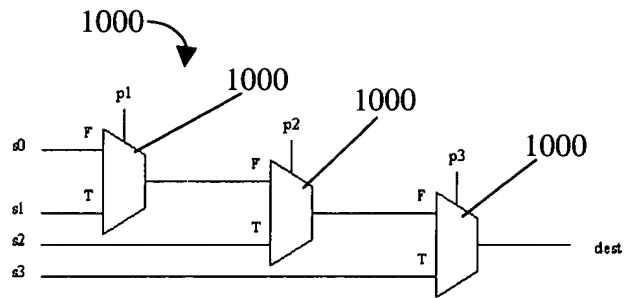


Fig. 10

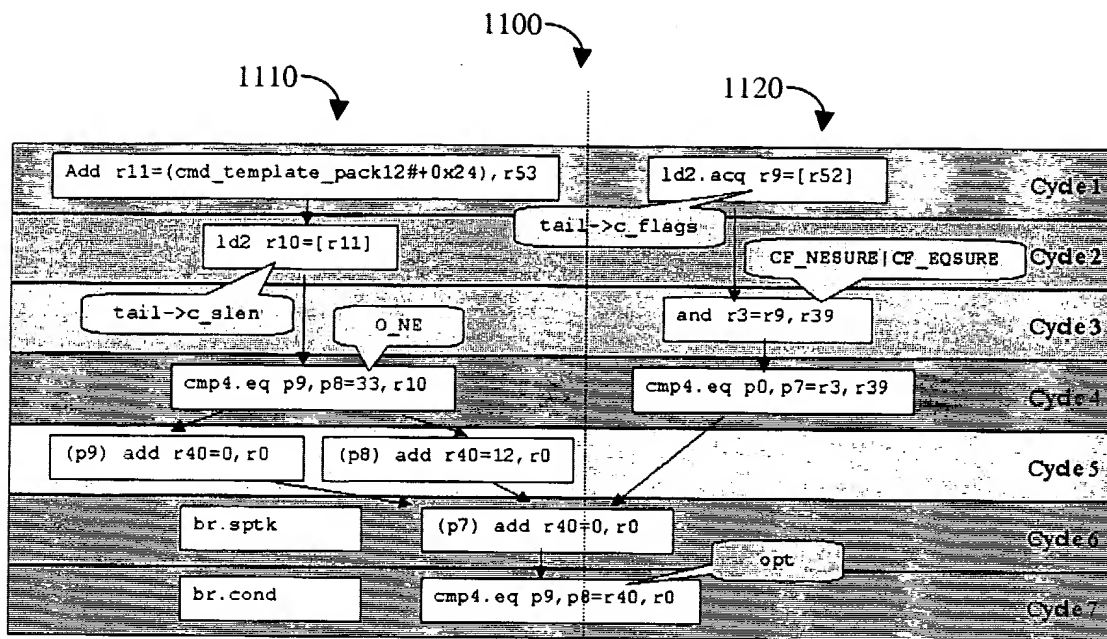


Fig. 11

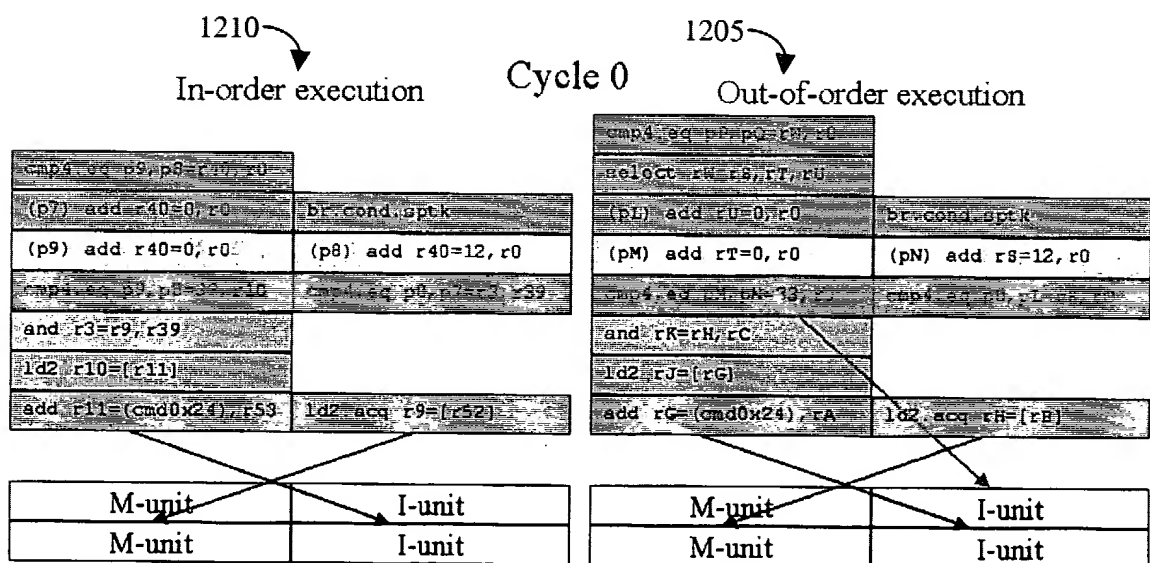


Fig. 12A

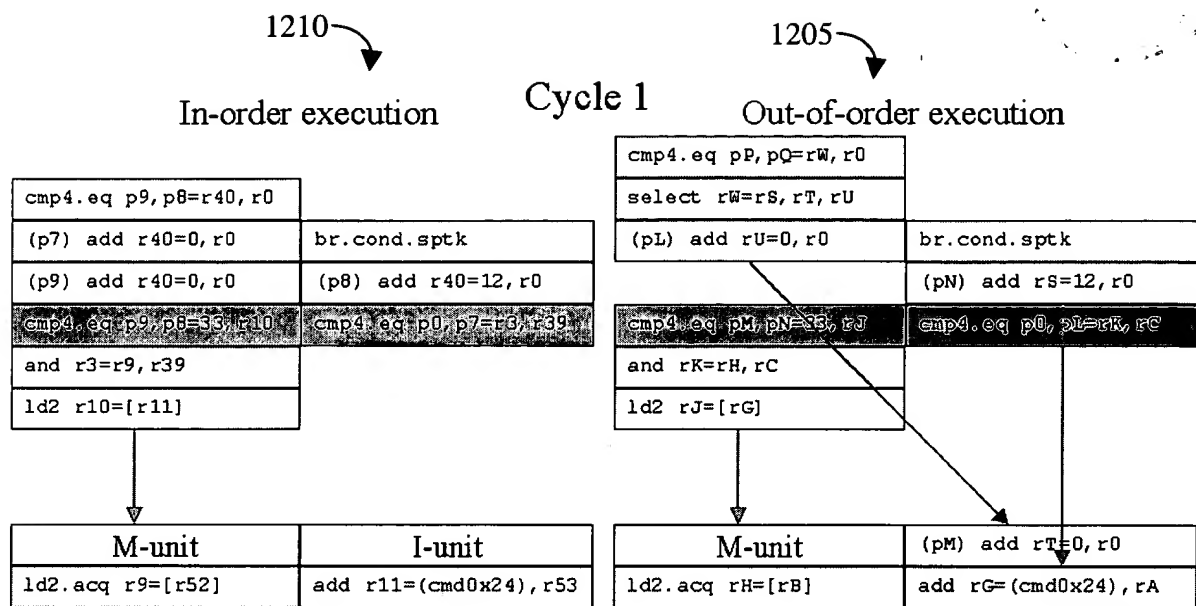


Fig. 12B

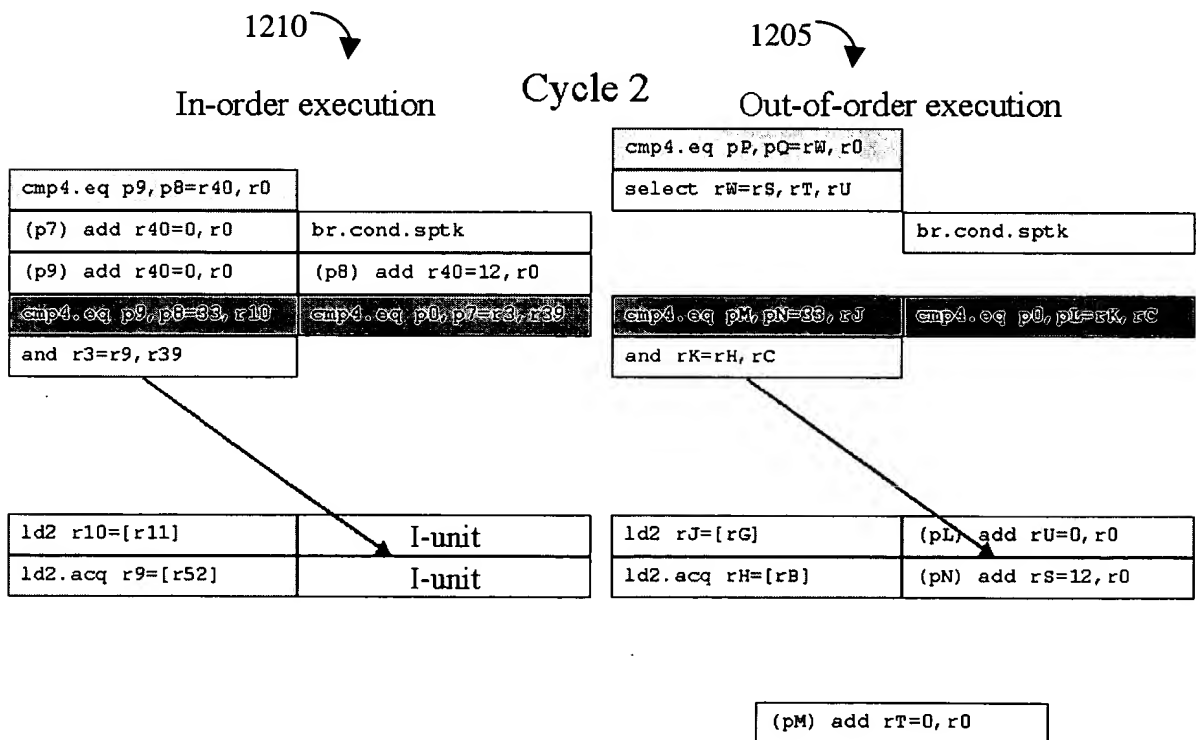


Fig. 12C

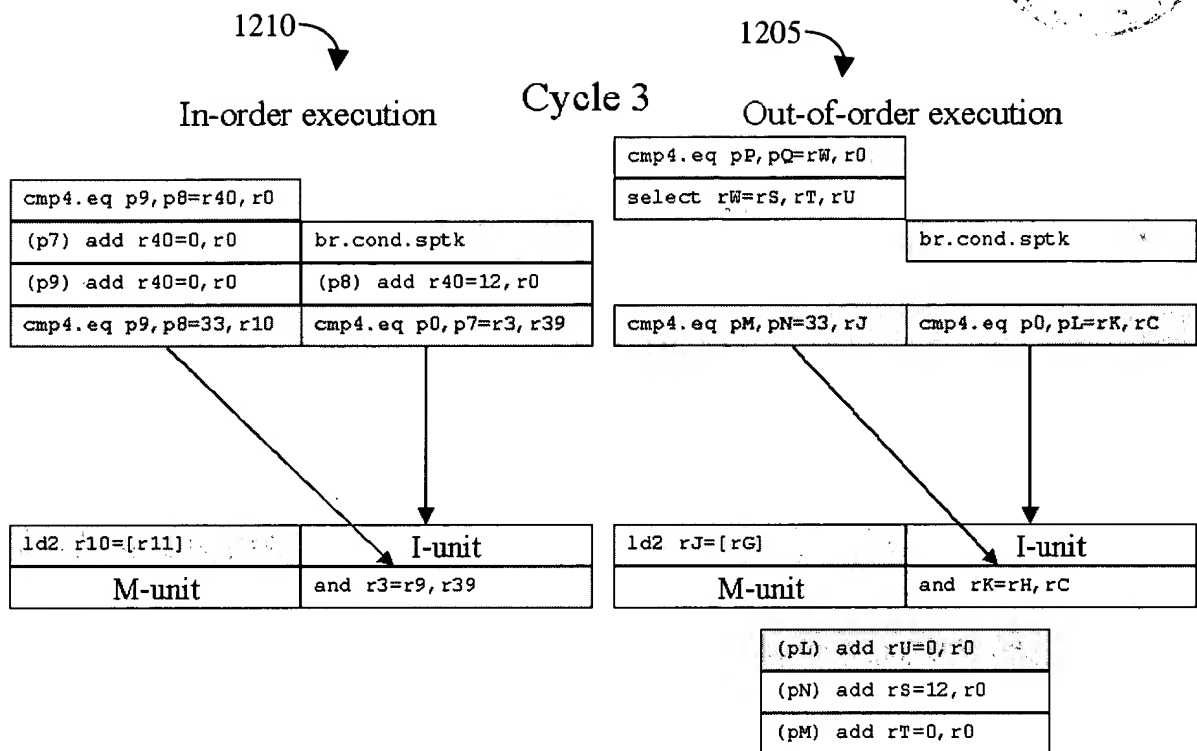


Fig. 12D

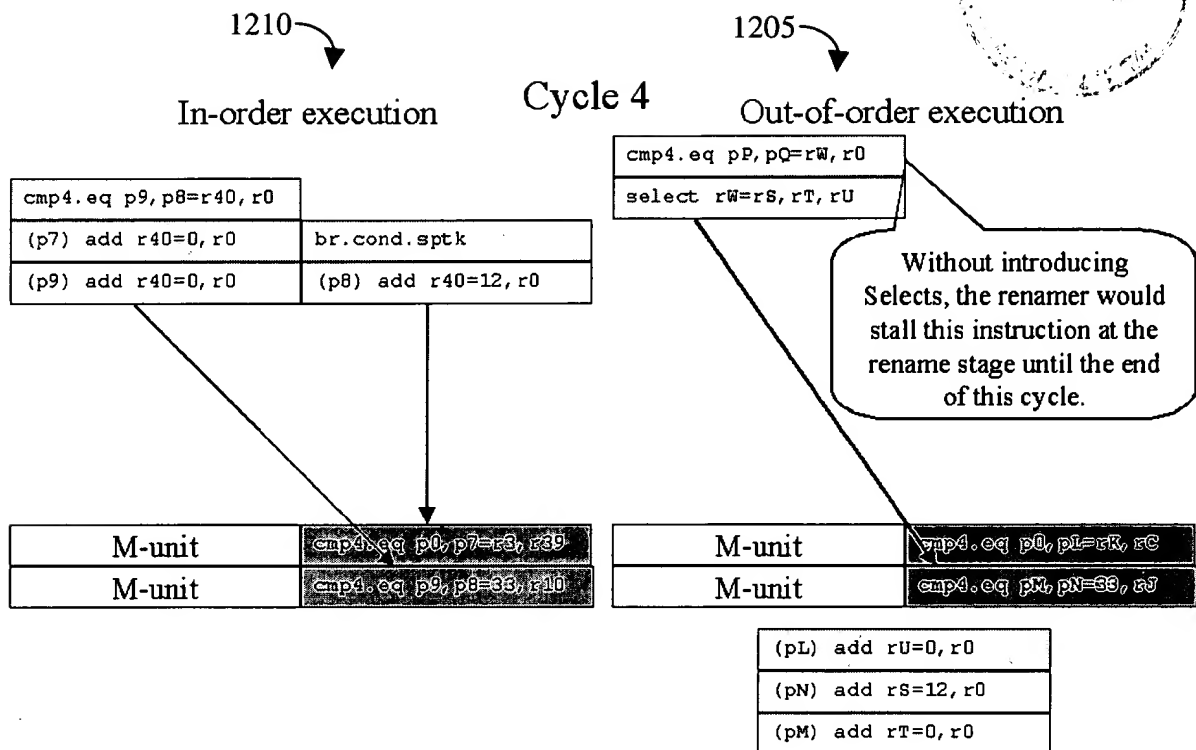


Fig. 12E

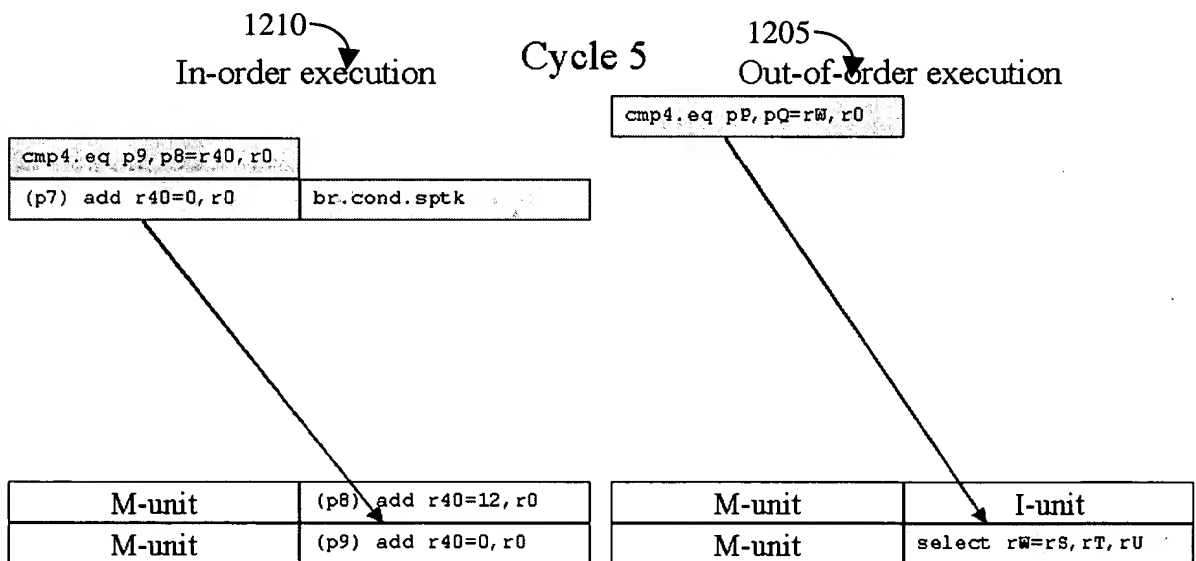


Fig. 12F



1210
In-order execution

Cycle 6

1205
Out-of-order execution

cmp4.eq p9, p8=r40, r0

M-unit	I-unit
M-unit	(p7) add r40=0, r0

M-unit	I-unit
M-unit	cmp4.eq pP, pQ=rW, r0

Fig. 12G